OPAL-RT TECHNOLOGIES

HIL real-time simulators Applications and benefits for ECU controllers validation







- CORPORATE PRESENTATION
- REAL-TIME SIMULATORS: METHODOLOGY & BENEFITS

NICE TO MEET YOU!





Christy Genganantha

Business Development Manager UK OPAL-RT Europe

christy.genganantha@opal-rt.com



Yoann Mougenot

Sales Director EMEA OPAL-RT Europe

yoann.mougenot@opal-rt.com









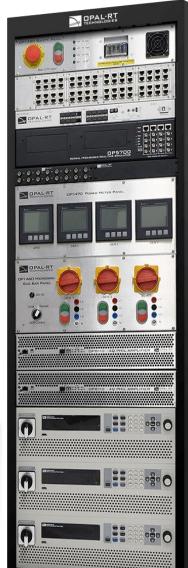
WHAT WE DO

REAL-TIME DIGITAL SIMULATORS











OBJECTIVES FOR OUR CUSTOMERS DESIGN, TEST & VALIDATE COMPLEX CONTROL SYSTEMS BEFORE PHYSICAL TESTING







BMS



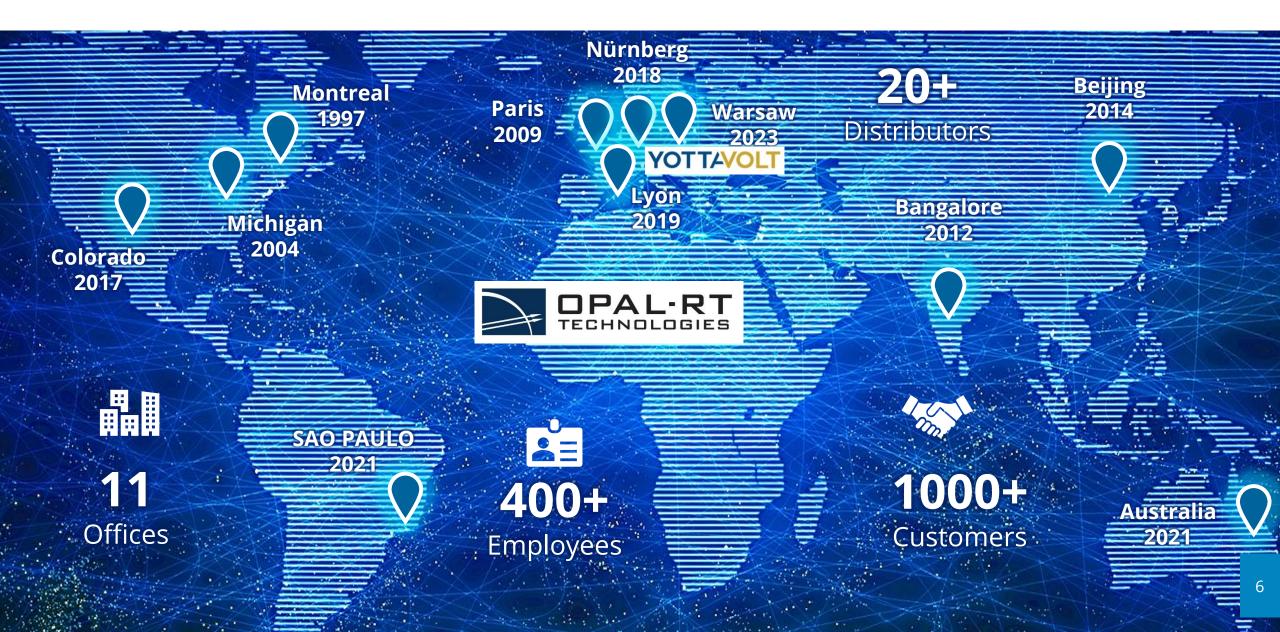
CONTROL ALGORITHMS



MICROGRID CONTROL







Trainings

- Trainings which allow our customer to use real-time simulators autonomously
- On-site trainings / e-learning platofrm

Technical support

- Local field application engineers teams
- Bring support to our customers

Real-time simulation expertise

- Models development
- Consulting
- Specific trainings

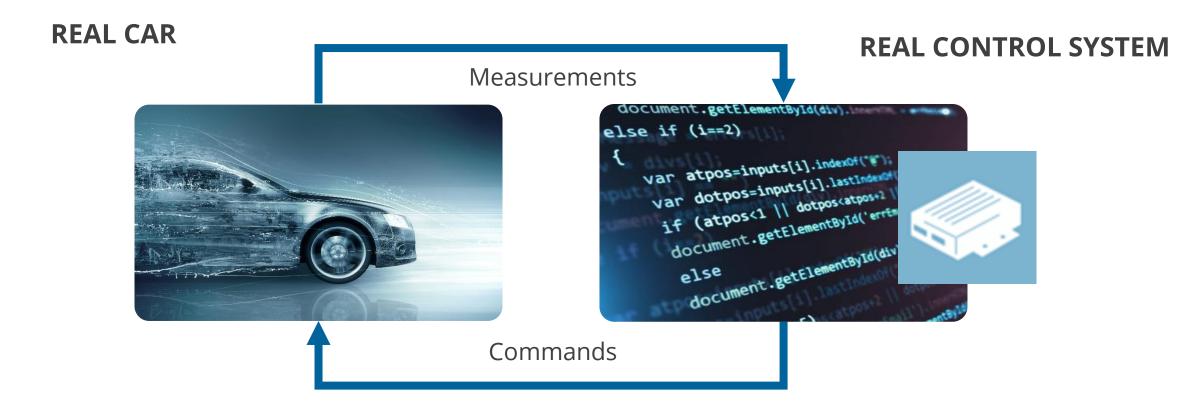




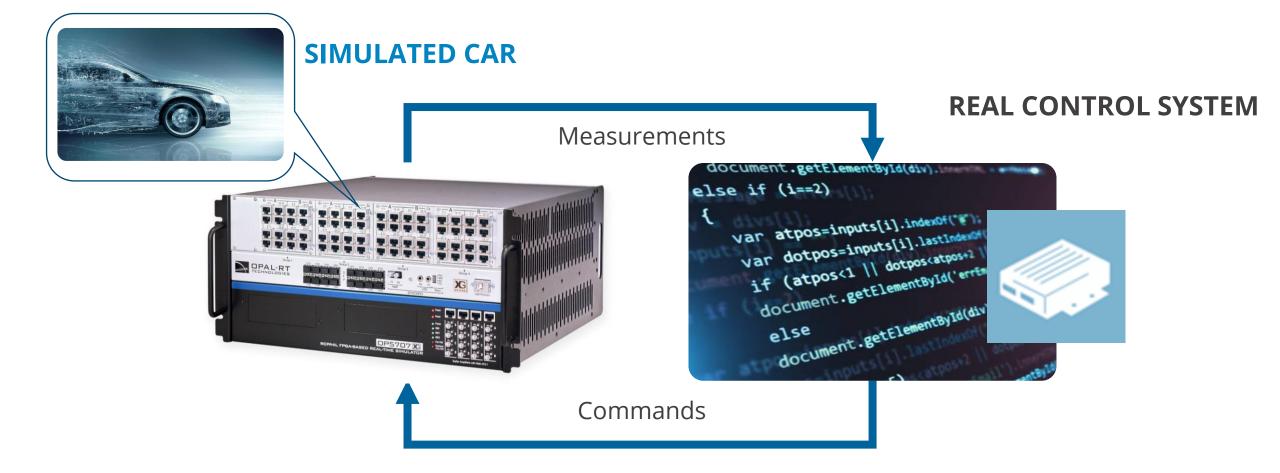




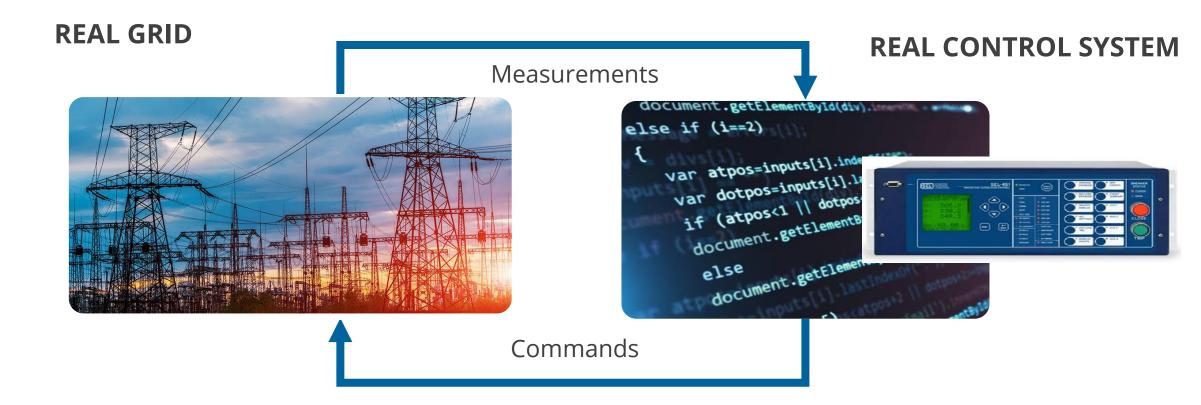




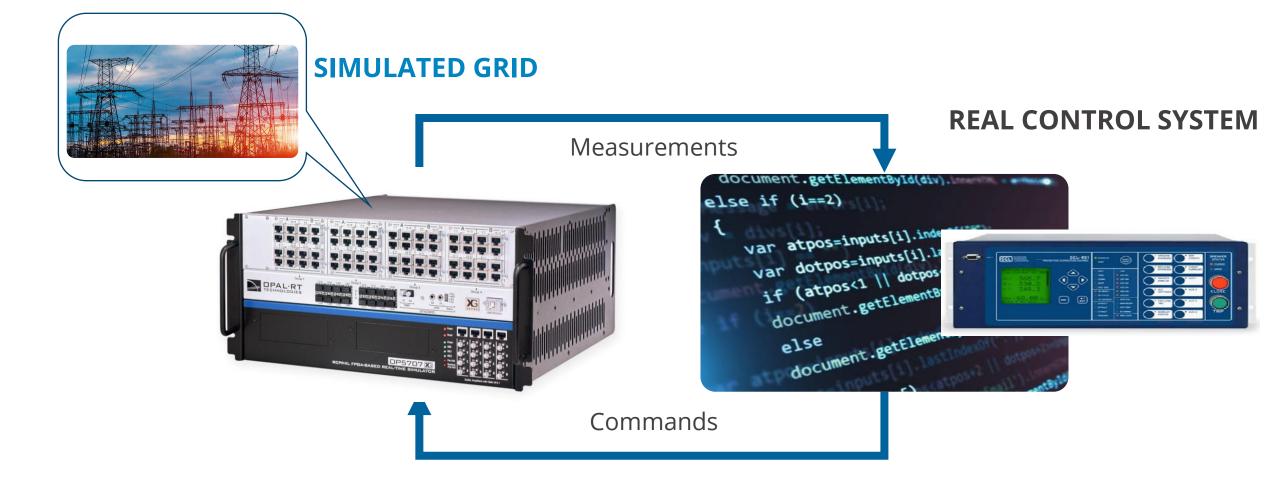












BENEFITS FOR OUR CUSTOMERS SAVE TIME, RISK & COST BY TESTING EARLIER NEW CONTROL DEVICES



DELIVERY

VALIDATION











OUR CUSTOMERS (SAMPLE)







Real-time simulation software

RT-LAB / HYPERSIM / VeriStand

Customer can import his/her model to be simulated or create it from scratch

Graphical user interface to perform test

+ Real-time simulator +

Processor cores & FPGA to simulate model in real-time

Inputs/outputs interfaces and sensors to connect devices under test

To test control devices

Device is lured thanks to RTS
Controllers (ECU, protections, SCADA)
Electrical machines
Real power devices





SNCF Train traction Simulink model





WORKSTATION



REAL-TIME SIMULATOR

Train traction Controller

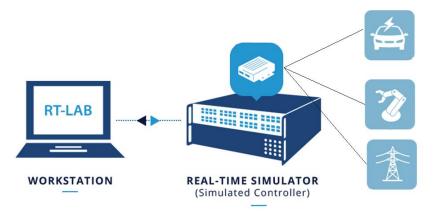


ACTUAL SYSTEM

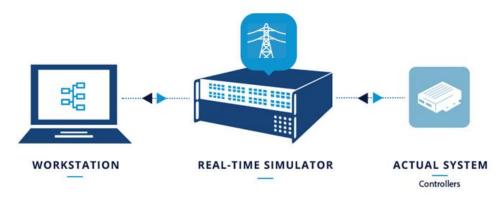




MODEL/SOFTWARE-IN-THE-LOOP



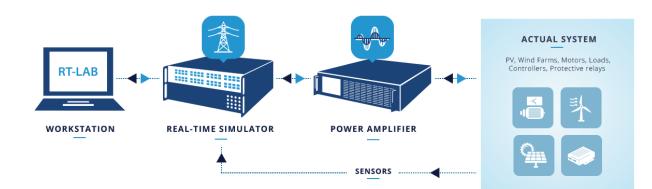
HARDWARE-IN-THE-LOOP



RAPID CONTROL PROTOTYPING



POWER HIL







OP5707XG

- From 4 to 32 INTEL® CPU cores (XG)
- XILINX® FPGA board Virtex 7
- Up to 8 I/O boards
- Up to 4 3rd part boards (Sync, Ethernet)
- SFP Modules for optical fiber
- More information



OP4610XG

- 6 CPU cores (XG)
- XILINX[®] FPGA board Kintex 7
- Up to 4 I/O boards
- Up to 2 3rd part boards (Sync, Ethernet)
- SFP Modules for optical fiber
- More information



OP4512

- 4 INTEL® CPU cores
- XILINX® FPGA board Kintex 7
- Up to 4 I/O boards
- 1 3rd part boards (Sync, Ethernet)
- SFP Modules for optical fiber
- More information



Analog outputs

16 channels [-16V; +16V] DAC: 16-bits, 1 or 2 Msps

Analog inputs

16 channels [-20V; +20V] ADC: 16-bits, 0.4 Msps



Digital outputs

32 channels PWM or Static digital « ON »: 5 to 30 V

Digital Inputs

32 channels PWM or Static digital « ON »: 4.5 to 50 V



DB37 + Screw terminals Ethernet ports / Specif. Connect. (Real-time simulator rear part)

Digital inputs/outputs

32 I/O reconfigurable channels PWM or Static digital « ON »: ± 50V





Easy access to I/O

(Access on real-time simulator top part)

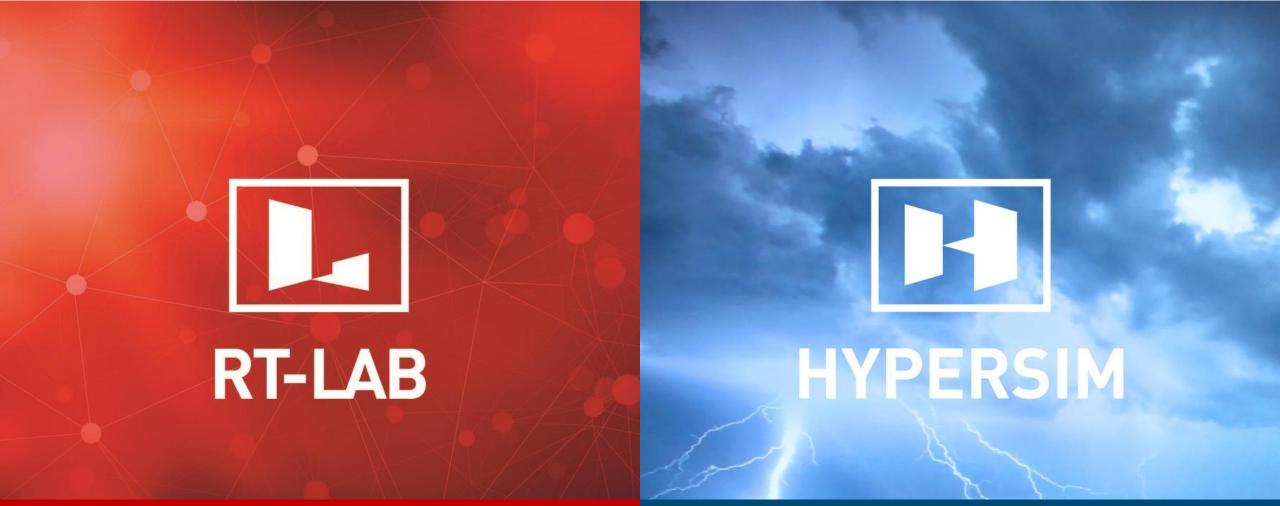


Supported communication protocols (partial list)

- IEC61850 GOOSE, SV & MMS
- C37.118
- DNP3
- IEC 60870-5-104
- TCP/IP, UDP/IP
- Modbus
- RS485, RS232
- CAN bus
- Aurora







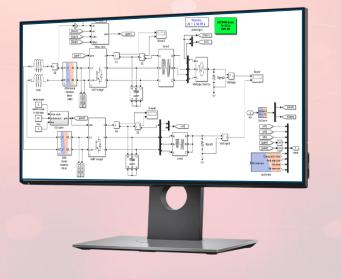
POWERFUL POWER SYSTEMS SIMULATION PLATFORM

MULTI-DOMAIN, FLEXIBLE, SCALABLE SIMULATION PLATFORM





RT-LAB enhances the capabilities of **Simulink** models to enable their execution in **real-time** and interface them with **real external hardware**. Supports virtually any kind of application.



SPECIALIZED ADD-ONS





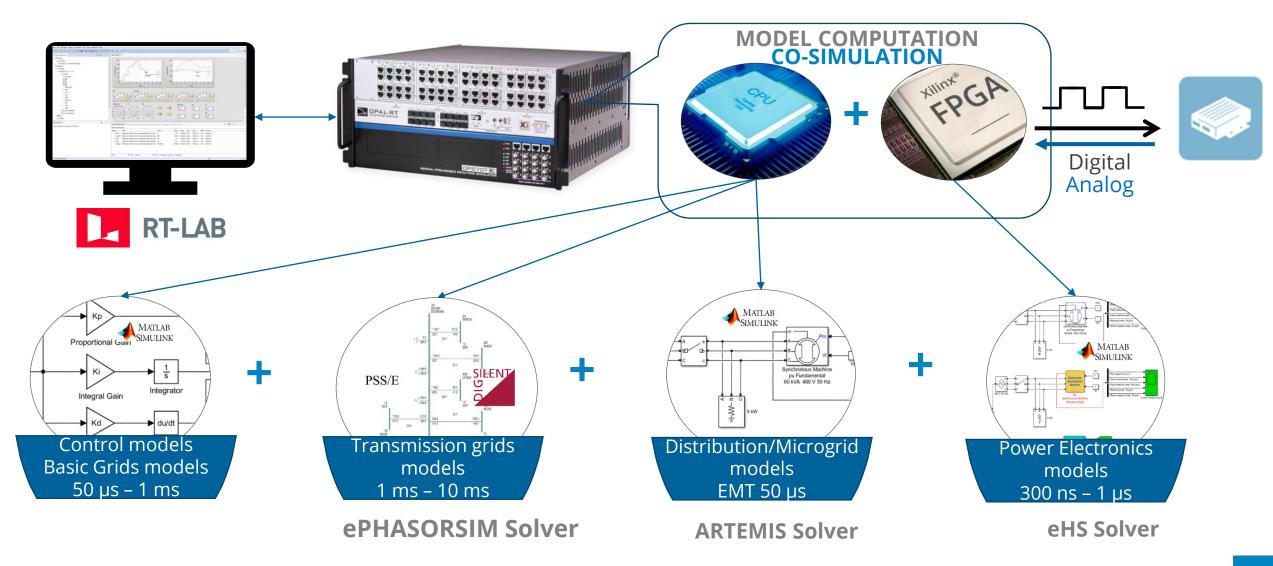
detailed simulation of electrical systems

average simulation of large electrical grids

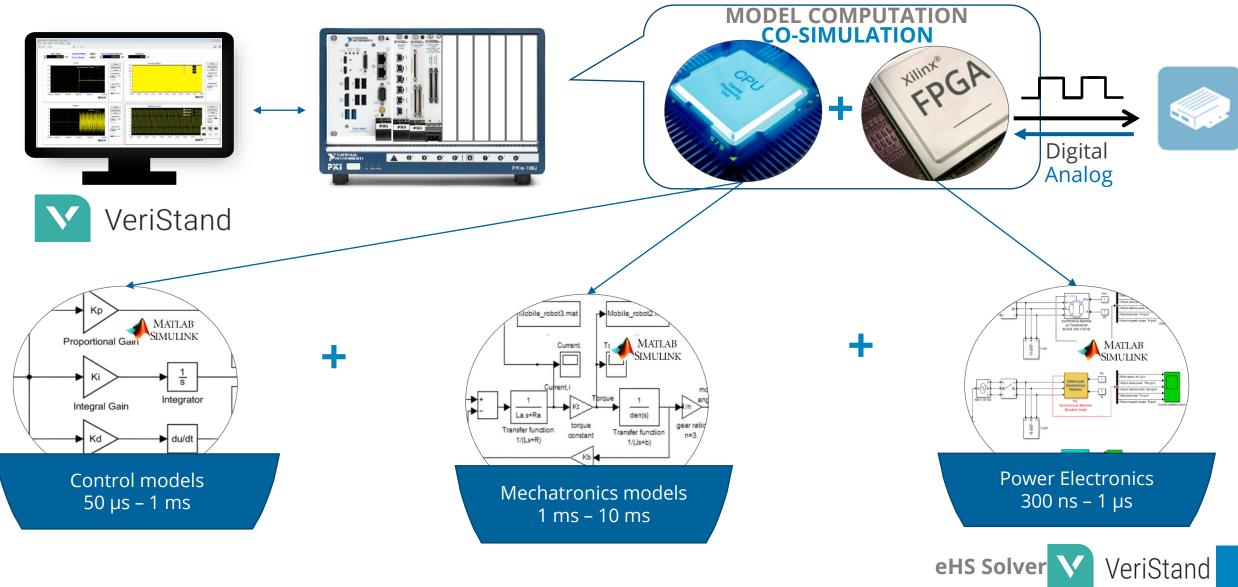


detailed simulation of very fast electrical systems Compatbile with NI Veristand & PXI chassis









eHS | Power Electronics Add-On





CONCLUSION





Complex industrial electrical and electromechanical systems Complex systems studied through virtual models



Fast evolution of technologies Flexibility of models which can be upgraded



More complex and sophisticated control strategies Control can also be simulated, as well as its interactions with the plant system



Huge validation effort required Validation can be progressive, from design phase and highly automated



On-field tests hard to conduct Many tests can be carried out in the lab with a virtual environment

Uniqueness of some projects Models can be adapted to any unique project



Belated validations can compromise the project Validation can be progressive, from design phase



Shorter development time More lab tests starting early reduce need for rework or waiting for parts

Dangerous test cases Dangerous scenarios are simulated and studied thouroughly

Cost optimisation



Less prototypes, less destructive tests, more virtual configurations, less risk

THANK YOU!

MORE INFORMATION Contact: Yoann.mougenot@opal-rt.com

Tel. +34 691 845 272

